



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Mun-Mo Jeong, et al.

Conf. No.

2385

Serial No.:

10/636,131

Examiner:

not yet assigned

Filing Date:

August 6, 2003

Art Unit:

2811

Title:

BITLINE OF SEMICONDUCTOR DEVICE HAVING STUD TYPE CAPPING

LAYER AND METHOD FOR FABRICATING THE SAME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

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Action)

Sir

This Information Disclosure Statement is submitted: under 37 CFR 1.97 (b); or (Within three months of filing national application; or date of entry of International application; or before mailing date of first office action on the merits; whichever occurs last) under 37 CFR 1.97 (c) together with either a: П Statement under 37 CFR 1.97 (e), or a \$180 fee under 37 CFR 1.17 (p); or (After mailing of first Office Action, but prior to Notice of Allowance or Final Office

 \boxtimes Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA

22313-1450

Adrienne Chocholak



PATENT APPLICATION Docket No. 8750-044 **Client No. SPX200208-0018US**

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INFORMATION DISCLOSURE CITATION FORM PTO-1449 (Modified)

		FOR	EIGN PATENT	DOCUMENTS	
Exam		Document	Publication		
<u>Init</u>	Ref	<u>Number</u>	<u>Date</u>	Country	<u>Name</u>
		2001-55685	7/4/2001	Korea	
Exam			OTHER DOCU	<u>JMENTS</u>	
<u>Init</u>	Ref	Author, Title, Da	te, Pertinent Page	es, Etc.)	
		English language on 7/4/2001.	of Abstract for I	Korean Publication	n No. 2001-55685, published
Examiner:					
Date Consi	dered:				